

1 "Referring to Fig. 5A, a typical packet group **50** in
2 the timing stream is illustrated. The packet group consists
3 of four packets **502**, each packet **502** having an 8 bit
4 payload. In the preferred embodiment, an addressable memory
5 location in the host processing unit stores 32 bits. The 2
6 bit control signals ~~indicated~~ indicate that what is being
7 transmitted in the timing trace stream is a series of 8 bit
8 payload packets. As indicated above, the timing trace
9 stream includes periodic sync markers that can synchronize
10 the plurality of trace streams."

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12 Please delete the Paragraph beginning on Line 21, Page 18
13 and replace that Paragraph with the following Paragraph.

14
15 "As indicated in Fig. 5B, the standard count is
16 included in an information packet. A second packet is
17 needed because it may ~~be expedite for the~~ testing of
18 different target devices to be capable of by storing a
19 programming signal group in the information packet. In
20 addition, the logic unit may be chosen to identify ~~one~~ more
21 than one standard count of clock eye cycles. In this
22 embodiment, the logic device can identify the number of
23 standard count of clock cycles and enter this number in
24 information packet. In this embodiment, the compressed
25 timing packet group is transferred to the FIFO unit when,
26 after the first standard count of clock cycles is
27 completed, a different logic value is identified."

1 Please delete the Paragraph beginning on Line 17 of Page 19
2 and replace that Paragraph with the following Paragraph.

3
4 "While the present timing trace stream has used the
5 control signals to describe the function of the associated
6 packet, the used ~~o-packets~~ packet groups with header could
7 also be used to interpret the payload of the packet. The
8 present invention provides a technique for compressing this
9 timing trace stream format."